



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE:: WARANGAL – 15
(An Autonomous Institute under Kakatiya University, Warangal)

MoM of ECI BoS

MINUTES OF THE MEETING

OF

BOARD OF STUDIES (VIRTUAL MEETING)

HELD AT 2:30 PM ON 22.05.2021 (Saturday)

Agenda:

1. Approval of Scheme of B. Tech. V and VI Semesters
2. Approval of Syllabus of B. Tech. V and VI Semesters Courses. Any other item
3. with the permission of chair.

BoS Members Present:

S. No	Name of the Member	Designation	Position in BoS
1.	Dr. B. Rama Devi	HoD ECE, KITSW	Chairperson, BoS
2.	Dr. K. Ashoka Reddy	Senior Professor of ECE Dept. KITSW	Member
3.	Dr. G. Raghotham Reddy	Professor of ECE Dept. KITSW	Member
4.	Dr. K. Sivani	Professor of EIE Dept. KITSW	Member
5.	Dr. M.Raghuram	Assoc. Prof. of ECE Dept. KITSW	Member
6.	Dr. P. Chandra Sekhar	Professor & Head, Dept. of ECE, University College of Engineering, OU, Hyderabad	External Member (from renowned Academic Institute)
7.	Dr. T.Kishore Kumar	Professor, NIT, Warangal	External Member (from renowned Academic Institute)
8.	Prof. T. Srinivasulu	Professor in ECE Department of Electronics and Communication Engineering Kakatiya University College of Engineering and Technology, Kakatiya University, Warangal-506009, India. Dean (Faculty of Engineering & Technology), Kakatiya University.	External Member (University Nominee)
9.	Sri R.Dileep	Sr. ASIC Design Engineer, Synopsis Inc, Pvt Ltd, Hyderabad.	External Member (from Industry)
10.	Dr. Y. Jagan Mohan Reddy	Senior Engineering Manager, Honeywell Technology Solutions Labs Pvt Ltd, Hyderabad	External Member (from Industry)
11.	Sri Bhaskar	Senior Staff Engineer Manager, Qualcomm	External Member (from Industry)
12.	Sri Gopi Chandar Reddy	Intel, Hyderabad	External Member (Post Graduate Meritorious Alumnus - Academia/Industry)
13.	Sri.E. Suresh	Assoc. Prof. of ECE Dept. KITSW	Co-OptedMember-1
14.	S. P. Girija	Assoc. Prof. of ECE Dept. KITSW	Co-OptedMember-2
15.	Dr. K.Venumadhav	Professor of EIE Dept. KITSW	Co-OptedMember-3

Resolutions:

- 1 Virtual BoS meeting of Electronics Communication and Instrumentation Engineering Department was conducted on 22.05.2021 from 2:30 pm to 4:30 pm through Google meet.
2. The above mentioned BoS members are present and offered their valuable suggestions.

3. BoS members of Electronics Communication and Instrumentation, KITSW approved

a. Scheme of B. Tech. V and VI Semesters

b. Syllabus of B. Tech. V and VI Semesters Courses & made following suggestions to incorporate.

1.	U18CI502A:	Artificial Intelligence and Machine Learning	<p><u>Prof. T. Srinivasulu</u> Make ML&AL as regular subject instead of Professional Elective</p> <p><u>Dr. Y. Jagan Mohan Reddy</u> Change the course name to Machine Learning & Artificial Intelligence.(ML &AL)</p> <p>Introduce the concepts of ML then followed by AL. (Teach first ML then AL-.Since ML is subset of AL)</p> <p>If possible include Deep Learning concepts.</p> <p><u>Dr. T.Kishore Kumar</u> Include few Applications of AL&ML in the 4th unit. (contact <u>Dr. Y. Jagan Mohan Reddy</u> for suggestions)</p>	Suggestions incorporated in the syllabus
2.	U18CI502B	Antennas and Wave Propagation	Unanimously approved	-
3.	U18CI502C:	Data Acquisition And Signal Conditioning	Unanimously approved	-
4.	U18CI502M:	MOOC Course	Unanimously approved	-
5.	U18CI503	Analog and Digital Communications	Unanimously approved	-
6.	U18CI504	Embedded System Design	Syllabus Unanimously approved, as presented	-
7.	U18CI505	Linear Integrated Circuits and Applications	Unanimously approved	-
8.	U18CI506	Embedded System Design Laboratory	<p><u>Dr. Y. Jagan Mohan Reddy</u> Modify the course name as Embedded Firmware Development Laboratory in scheme</p> <p>Syllabus Unanimously approved, as presented</p>	Suggestions incorporated in the syllabus

9.	U18CI507	Analog and Digital Communications Laboratory	Syllabus Unanimously approved, as presented	-
10.	U18CI508	Linear and Digital Integrated Circuits Laboratory	Syllabus Unanimously approved, as presented	-
11.	U18CI510	Seminar	Unanimously approved	-

12.	U18CI 603A :	Internet of things	Unanimously approved	-
13.	U18 CI 603B:	Wireless Sensor Networks and Applications	Syllabus Unanimously approved, as presented	-
14.	U18 CI 603M:	MOOC Course	Unanimously approved	-
15.	U18 CI 604	Embedded Systems with ARM Processor	Unanimously approved	-
16.	U18 CI 605	VLSI System Design	Sri R.Dileep Remove the content related to sub-system designing in Unit 3. Include Verilog instead of system Verilog as 4th unit. If possible increase the content of front end design concepts. (i.e Unit 3 and 4 focusing on verilog/System Verilog)	Suggestions incorporated in the syllabus
17.	U18 CI 606	Wireless and Data Communication	Unanimously approved	-
18.	U18 CI 607	Digital Design Laboratory	Sri R.Dileep Limit the Combinational and Sequential design using Verilog based experiments to 5. Include another 5 has System Verilog based experiments to impart the verification environment knowledge. (Condition if student studies the System Verilog concepts in VLSI System Design Course and also availability of tool.)	Suggestions incorporated in the syllabus
19.	U18 CI 608	Embedded Systems with ARM Processor Laboratory	Unanimously approved	-
20.	U18 CI 609	Embedded networking and Application Laboratory	Unanimously approved	-
21.	U18CI610	Mini Project	Unanimously approved	-

Finally, Dr.B.Rama Devi, Professor, Head and Chairperson, BoS, ECE Department, thanked all the ECI BoS members, Principal, AAC members and faculty of Electronics and Communication Engineering Department for giving their suggestions while preparing the scheme and drafting the syllabus.

Photo gallery of Virtual BoS meeting: